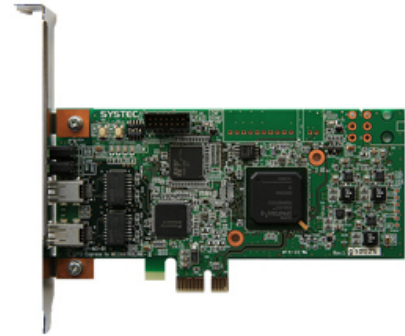


SYSTEC Corporation

PCI Express MECHATROLINK-III Interface Card

Features

- By connecting to a PCI Express slot (PC, etc.) with a dedicated MECHATROLINK-III communication driver, a PC based High-Speed and High-Performance control can be achieved.
- The Cyclic Communication of MECHATROLINK-III ASIC is automatically controlled by FPGA.
- The Command/Response data between PC and MECHATROLINK-III ASIC is automatically transmitted by the DMA function of the FPGA. (Using a dedicated library for automatic transfer)
- Supports Master/Slave External Synchronization.
- API library for INtime is separately provided.
- Support up to 62 node connection / 62 axis controls. (The maximum number of connection depends on the host application)



SY-M3-01

Specifications

Item	Specifications
Bus standard	Gen1×1 PCI Express bus
Communication	MECHATROLINK-III Master (C1 Master)
No. of MECHATROLINK channels	1 (IMI Connector×2)
External synchronization	With optional connector, 1 master can control 5 slaves.
Supported OS	INtime5 or later
Power supply	12V (PCI Express slot power)
Board size	130mm × 69mm (Supports Low Profile bracket)

MECHATROLINK specifications

		M-III			
		16-byte	32-byte	48-byte	64-byte
Profile	Standard servo	-	×	×	-
	Standard I/O	×	×	×	×
	Standard Stepping Motor Drivers	-	×	×	-
	Standard Inverter	-	×	-	×
Transmission cycle		31.25 μs to 8ms		62.5 μs to 8ms	

Contact Information

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