

YASKAWA ELECTRIC CORPORATION

MECHATROLINK-4/MECHATROLINK-III Master/Slave Communication LSI JL-L000A

Features

- A product with MECHATROLINK-4 can develop easily in the combination of this LSI and Host CPU.
- It also supports MECHATROLINK-III.
- A FLASH memory dedicated to this ASIC is also included.
- RoHS-compliant



JL-L000A

Specifications

| Item | Specification |
|--------------|--|
| Model | JZPMC-MD001A-60P |
| Application | MECHATROLINK-4/MECHATROLINK-III master/slave |
| Packing unit | 60 pcs. |
| Dimension | JL-L000A : PFBGA (0.8mm pitch) 481pin 21x21mm |
| Notes | JL-L000A dedicated FLASH (8pin SOP 5.2x7.9 mm) are included(60pcs.). |

Attention

To purchase this LSI, it is necessary to join the MECHATROLINK members Association (MMA) with Board, Executive, User, Regular Membership.

Contact Information

Please access [YASKAWA website \(Please click. Then you can access.\)](#)

YASKAWA has sales and after-sales service all around the world.

America: U.S.A., Brazil, Canada, Mexico

Asia Pacific: China, Korea, Taiwan, Singapore, India

Europe: Germany, Sweden, Israel, Italy, France, Spain, Finland, Netherlands, Slovenia, Czech republic, Turkey